

MOSCONI DSP6T08



Technical Specifications

- INPUT MODE: Low Level (LL) / High Level (HL)
- INPUT IMPEDANCE: > 10Kohm [Typ 14.3Kohm] (LL) / 47ohm (HL)
- INPUT SENSITIVITY: 2Vrms ÷ 8Vrms (LL) / 5Vrms ⊕ ÷ 20Vrms (HL) - via on-board potentiometers
- INPUT VOLTAGE REGULATION: -12dB ÷ 0dB - via on-board potentiometers
- MAX OUTPUT SIGNAL: 4Vrms
- OUTPUT IMPEDANCE: 50 ohm
- THD DISTORTION: <0.003% @ 1Vrms
- FREQUENCY RESPONSE: 20Hz ÷ 20KHz (within -0.1dB)
- CHANNEL SEPARATION: >75dB (@ 1KHz)
- SUPPLY VOLTAGE: 11V ÷ 15V
- CURRENT CONSUMPTION: 0.5A @ 12V
- DC-DC CONVERTER: transformer coupled, Voltage regulated, Current-Mode, Flyback converter
- CONVERSION FREQUENCY: 150KHz ÷ 200KHz
- GROUND ISOLATION vs CAR BODY: 1Kohm
- AUTOSENSE: two operative modes (yes, HL only)
- REMOTE OUTPUT: 50mA max.
- COMMUNICATION PERIPHERALS: on-board USB, Bluetooth socket, Remote controller
- EXPANSION SLOT: External Memory (EEPROM).
- OPTICAL BOARD: Optional.
- SIZE: 150 x 40 x 125 mm (including fixing feet).

Processor Specifications

Data bus I2S (DAC/ADC-Performance) 24 Bit / 96kHz

- 8 channel Programmable Audio Digital Signal Processor (DSP)
- 135-MHz Maximum Speed, >1400 Processing Cycles Per Sample at 96 kHz
- Sample Rates of 32 kHz to 192 kHz.....-> 6to8: 48kHz
- **48-Bit Data Path** and 28-Bit Coefficient.....
- Single-Cycle, **76-Bit Multiply** Accumulate..... Five Simultaneous Operations Per Clock Cycle
- 1022 Words of 48-Bit Data Memory
- 1022 Words of 28-Bit Coefficient Memory
- 3K Words of 54-Bit Program RAM
- 5.88K Words of 24-Bit Delay Memory (122.5 ms at 48 kHz)
- Signal transmit time <0,5 ms (very very fast)